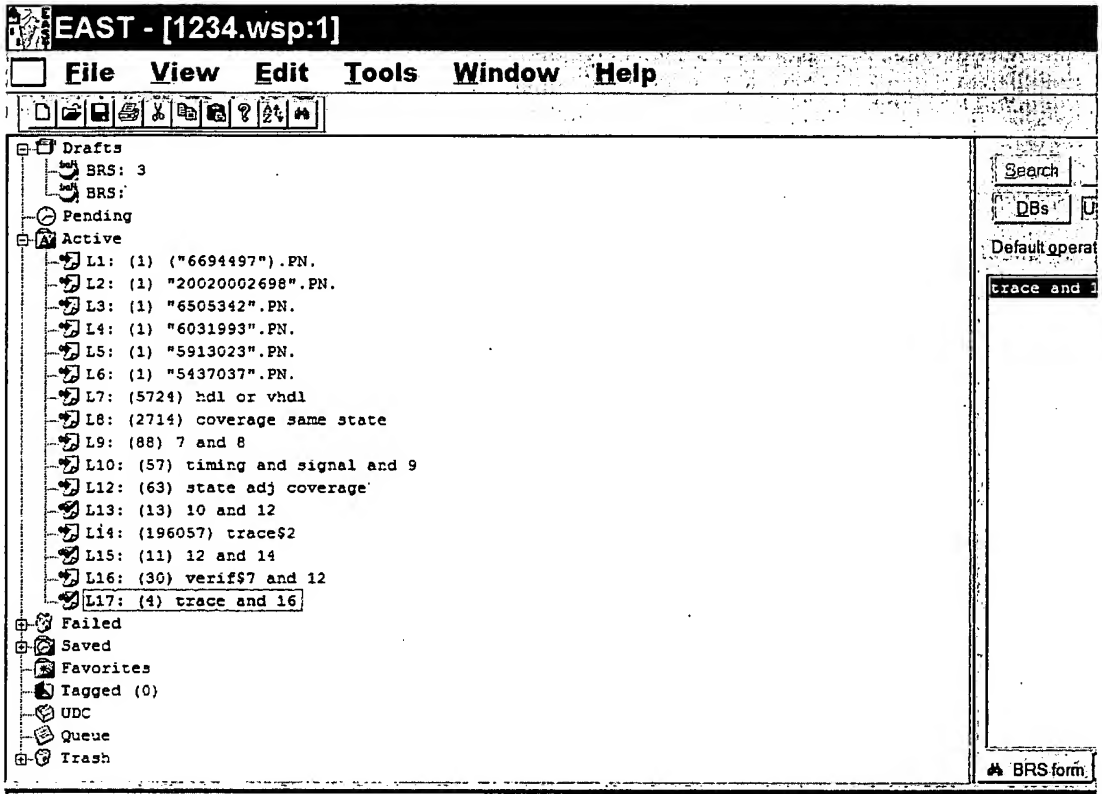
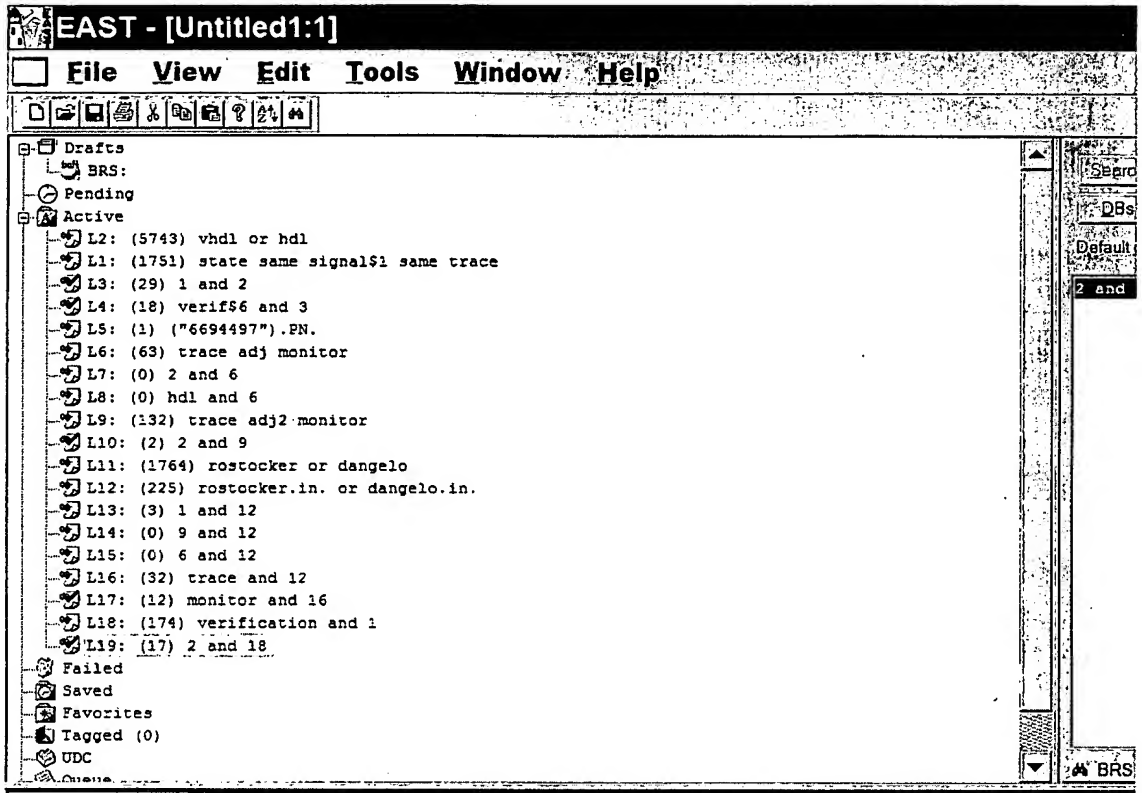
Subject:



	U	I	Document ID	Issue Date	Pages	Title	Current OR	C
1	☐	☐	US 6782518 B2	20040824	21	System and method for facilitating coverage feedback testcase	716/5	703 714
2	☐	☐	US 6484134 B1	20021119	13	Property coverage in formal verification	703/14	703 716
3	☐	☐	US 4945410 A	19900731	19	Satellite communications system for medical related images	725/67	358 725
4	☐	☐	US 4802008 A	19890131	18	Satellite communications system for medical related images	725/67	348 358



	U		Document ID	Issue Date	Pages	Title	Current OR	Curre
1	☐	☐	US 6826717 B1	20041130	20	Synchronization of hardware and software debuggers	714/39	714/725
2	☐	☐	US 6732068 B2	20040504	51	Memory circuit for use in hardware emulation system	703/24	326/40; 703/23;
3	☐	☐	US 6694464 B1	20040217	61	Method and apparatus for dynamically testing electrical	714/725	714/735 714/736
4	☐	☐	US 6691287 B2	20040210	31	Functional verification system	716/4	703/14; 716/10;
5	☐	☐	US 6678841 B1	20040113	16	Function test support system and function test support method and	714/57	714/33
6	☐	☐	US 6629297 B2	20030930	30	Tracing the change of state of a signal in a functional verification	716/5	716/4; 716/7
7	☐	☐	US 6625786 B2	20030923	30	Run-time controller in a functional verification system	716/5	716/1; 716/4;
8	☐	☐	US 6510405 B1	20030121	17	Method and apparatus for selectively displaying signal	703/16	703/17; 714/37;
9	☐	☐	US 6480988 B2	20021112	29	Functional verification of both cycle-based and non-cycle based	716/5	703/16
10	☐	☐	US 6470480 B2	20021022	30	Tracing different states reached by a signal in a functional	716/4	324/528 324/759
11	☐	☐	US 6377912 B1	20020423	55	Emulation system with time-multiplexed interconnect	703/28	716/1